

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 10-24-2003
Application Type:: Regular
Subject Matter:: Utility
Title:: Method and Apparatus for Power
Optimization During an Integrated
Circuit Design Process
Attorney Docket Number:: X-1374 US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 05
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: Ireland
Status:: Full Capacity
Given Name:: Patrick
Family Name:: Lysaght
Street:: 16455 Ferris Avenue
City:: Los Gatos
State or Province:: California
Postal or Zip Code:: 95032

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: US
Status:: Full Capacity
Given Name:: Tim
Family Name:: Tuan
Street:: 3286 Payne Avenue
City:: San Jose
State or Province:: California

Postal or Zip Code:: 95117

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: Sweden
Status:: Full Capacity
Given Name:: Goran
Family Name:: Bilski
Street:: Ejdergatan 7
City:: Molndal
Country:: Sweden
Postal or Zip Code:: 43164

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

Representative Customer Number::	24309	
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ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124